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**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

Claim 1 (currently amended):      A method comprising:  
    updating an entry of a flush vector coupled to a filter, the flush vector including an entry for each entry in the filter, if a match occurs between a corresponding entry in the filter and an address;  
    invalidating [[an]] the entry of [[a]] the filter coupled to a pipeline resource if [[an]] the update to the entry of the flush vector occurs during a context; and  
    flushing a portion of the pipeline resource corresponding to an address space including the entry.

Claim 2 (original):      The method of claim 1, further comprising flushing the portion upon a switch from the context.

Claim 3 (original):      The method of claim 1, wherein the pipeline resource comprises a translation lookaside buffer.

Claim 4 (currently amended):      The method of claim 1, further comprising comparing [[an]] the address obtained from an external snoop to a plurality of entries in the filter to determine if the update has occurred.

Claim 5 (original):      The method of claim 1, further comprising flushing the portion of the pipeline resource via microcode.

Claims 6 - 10 (cancel)

Claims 11-16 (canceled)

Claim 17 (currently amended):      An apparatus comprising:

a pipeline resource having a plurality of address spaces, each of the plurality of address spaces corresponding to one of a plurality of contexts, each one of the plurality of address spaces selectively flushable while the other address spaces are maintained; and

a filter coupled to the pipeline resource to select at least one of the plurality of address spaces to be flushed, the filter partitioned into a plurality of partitions each corresponding to one of the plurality of address spaces, wherein each partition includes a static number of entries and at least one partition includes additional dynamic entries for at least one of the plurality of address spaces.

Claim 18 (original): The apparatus of claim 17, wherein the pipeline resource comprises a translation lookaside buffer.

Claim 19 (cancel)

Claim 20 (currently amended): The apparatus of claim ~~[[19]]~~ 17, wherein the filter comprises a content addressable memory.

Claim 21 (currently amended): A method comprising:  
dynamically partitioning a filter of a pipeline resource into a plurality of partitions, each of the partitions corresponding to one of a plurality of address spaces, wherein each of the plurality of partitions includes a fixed portion, the filter further including a dynamic portion to store entries of at least one of the plurality of address spaces.

Claim 22 (original): The method of claim 21, further comprising sharing the pipeline resource among a plurality of applications, each corresponding to one of the plurality of address spaces.

Claim 23 (cancel)

Claim 24 (currently amended): The method of claim [[23]] 21, further comprising allocating at least part of the dynamic portion to one of the plurality of applications that has consumed the fixed portion of one of the plurality of partitions.

Claim 25 (currently amended): An article comprising a machine-readable storage medium containing instructions that if executed enable a system to:

dynamically partition a filter of a pipeline resource into a plurality of partitions, each of the partitions corresponding to one of a plurality of address spaces, wherein each of the plurality of partitions includes a fixed portion, the filter further including a dynamic portion to store entries of at least one of the plurality of address spaces.

Claim 26 (original): The article of claim 25, further comprising instructions that if executed enable the system to permit a plurality of applications, each corresponding to one of the plurality of address spaces, to share the pipeline resource.

Claim 27 (original): The article of claim 26, further comprising instructions that if executed enable the system to allocate at least part of a dynamic portion of the filter to one of the plurality of applications that has consumed one of the plurality of partitions.

Claim 28 (currently amended): A system comprising:  
a first processor having a pipeline resource having a plurality of address spaces, each of the plurality of address spaces corresponding to one of a plurality of contexts, each one of the plurality of address spaces selectively flushable while the other address spaces are maintained, and a filter coupled to the pipeline resource to snoop address information from a second processor, the filter to select at least one of the plurality of address spaces to be flushed, and wherein the filter is partitioned into a plurality of portions each corresponding to one of the plurality of address spaces, wherein each partition includes a static number of entries and at least one partition includes additional dynamic entries; and

a dynamic random access memory coupled to the first processor.

Claim 29 (original): The system of claim 28, further comprising a second processor coupled to the first processor.

Claim 30 (cancel)

Claim 31 (new): The method of claim 1, further comprising clearing the flush vector after invalidating the entry.

Claim 32 (new): The method of claim 1, further comprising invalidating all entries of the filter corresponding to the address space.

Claim 33 (new): The method of claim 1, wherein invalidating the entry includes invalidating a first indicator of the entry during a first cycle and invalidating a second indicator of the entry during a second cycle.

Claim 34 (new): The apparatus of claim 17, further comprising a flush vector coupled to the filter, the flush vector including an entry for each entry in the filter, wherein an entry of the flush vector is updated if a match between an incoming address and an entry in the filter occurs.